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\*\* CONTINUING DATA *PJ**No*\*\* FOREIGN APPLICATIONS *PJ*

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35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
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## ADDRESS

23838

## TITLE

Method and apparatus for reducing memory latency in a cache coherent multi-node architecture

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